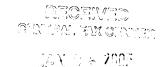
PATENT APPLICATION

ATTORNEY DOCKET NO. CSCO-010/4390

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Inventor(s): Amit S. Phadnis, Pankaj Vyas and Balaji Lakshmikanth Bangolae

Serial No.: 09/976,004 Examiner: OLMS, DOUGLAS W

Filing Date: 10/15/2001 **Group Art Unit: 2661**

Title: Reducing Overhead When Setting Up Multiple Virtual Circuits Using Signaling Protocols

Honorable Commissioner for Patents Alexandria, Virginia 22313-1450

STATUS INQUIRY

Sir	r:		
1.	More than 38 months have passed since:		
	[X] NEW APPLICATIONS the filing of this application on 10/15/2001. No Trademark Office indicating action on this app	communication has been received from Patent and lication.	
	[] AMENDED APPLICATIONS the filing of a response on from the Patent and Trademark Office.	No further communication has been received	
	[] APPEALED APPLICATION The Appeal Brief was filed on [] An Examiner's Answer was mailed on [] A Reply Brief to the Examiner's Answer w		
	[] ALLOWED APPLICATIONS the mailing of Form POL-327 and/or Examiner	's Amendment on	
2.	Kindly advise the undersigned of the present status of this application, by checking the appropriate box on th next page.		
		Respectfully submitted,	
		By Narendra R. Thappeta Attorney/Agent for Applicant(s)	
		Reg. No. 41416	
		Date: January 4, 2005	

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STATUS INQUIRY REPLY

Please include expected date of first office action

BY FACSIMILE TO: (707) 356-4172

APPLICATION SERIAL NO. <u>09/976,004</u> is currently				
[] Assigned to Group	and awaits:			
[] Action by the examiner				
[] Applicant's response to Office Action mailed				
APPEAL NO				
[] Awaiting action by the board of Appeals and	Interferences			
[] Date of hearing expected				
[] Decision expected	·			